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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gilbert MacDonald et al

Art Unit: 2838

Patent No. 6,775,163 B2

Serial No.: 10/661,216

Examiner: Berhane, Adolf D.

Filed: September 12, 2003

Docket: 101950.00140

For: **DUAL INPUT AC/DC TO PROGRAMMABLE DC OUTPUT CONVERTER**

**REQUEST FOR CERTIFICATE OF CORRECTION**

September 27, 2004

Commissioner For Patents  
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. 1.8(A)  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on September 27, 2004.

*Trish Paramore*  
Trish Paramore

Sir:

Enclosed please find a Certificate of Correction on the above identified patent.

No fee is deemed due at this time since the error is a printing error of the Patent and Trademark Office. The Commissioner is hereby authorized to charge any missing or insufficient fees filed, or asserted to be filed, or which should have been filed herewith, or credit any overpayment to Deposit Account No. 50-1752.

Respectfully Submitted,

*Robert C. Klinger*

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**Certificate**  
**OCT 05 2004**  
**of Correction**

**OCT 06 2004**

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,775,163 B2  
DATED : August 10, 2004  
INVENTOR(S) : Gilbert MacDonald, et al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6 line 55 "voltaae" should read --voltage --

Column 8 line 3 "topoology" should read --topology--

**MAILING ADDRESS OF SENDER:**

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PATENT NO. 6,775,163 B2

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OCT 06 2004